

WHAT IS CLAIMED IS:

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1. A selector circuit comprising:
a scan circuit for sequentially outputting a pulse
for selection; and

5 a decoder circuit for designating a desired block
of said scan circuit divided into a plurality of
blocks, so as to allow said scan circuit to start to
output the pulse from a predetermined position in the
designated desired block.

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2. A selector circuit according to claim 1,
further comprising reset means for resetting said scan
circuit to stop said scan circuit from sequentially
outputting the pulse.

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3. A selector circuit comprising:
scanning means for sequentially outputting a pulse
for selection;

20 first designating means for supplying a start
signal for a desired block of a predetermined area
divided into a plurality of blocks smaller than a whole
area of said scanning means, so as to allow said
scanning means to start to output the pulse from a
predetermined position in the desired block; and

25 second designating means for supplying a start
signal to start to output sequentially the pulse from
an area other than the predetermined area of said

scanning means.

4. A selector circuit according to claim 3,
further comprising control means for controlling said
first designating circuit to designate the desired
block after the pulse is sequentially output for the
area other than the predetermined area.

5. An image pickup apparatus comprising:
a plurality of pixels;
a scan circuit for sequentially outputting a pulse
for selection; and

a decoder circuit for designating a desired block
of said scan circuit divided into a plurality of
blocks, so as to allow said scan circuit to start to
output the pulse from a predetermined position in the
designated desired block.

6. An image pickup apparatus according to claim
5, further comprising reset means for resetting said
scan circuit to stop said scan circuit from
sequentially outputting the select pulse.

7. An image pickup apparatus comprising:
a plurality of pixels for obtaining image signals;
a plurality of optical black pixels for obtaining
a dark level;

scanning means for sequentially outputting a select pulse for selecting the plurality of pixels and the plurality of optical black pixels;

5 a first designating circuit for supplying a start signal for a desired block of said scanning means divided into a plurality of blocks to select the plurality of pixels, so as to allow said scanning means to start to output the select pulse from a predetermined position in the desired block; and

10 second designating means for supplying a start signal for sequentially outputting the select pulse to select the plurality of optical black pixels.

8. An image pickup apparatus according to claim 15 7, further comprising control means for controlling said first designating circuit to designate the desired block after the select pulse is sequentially output for selecting the plurality of optical black pixels.

20 9. An image pickup system comprising:

a plurality of pixels;

a scan circuit for sequentially outputting a select pulse for selecting each pixel;

25 a decoder circuit for designating a desired block of said scan circuit divided into a plurality of blocks, so as to allow said scan circuit to start to output the select pulse from a predetermined position

in the designated desired block;

an analog/digital converter circuit for converting
a signal from each of the plurality of pixels into a
digital signal; and

5 a signal processing circuit for processing a
signal output from said analog/digital converter
circuit.

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